

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Docket No.: 6161.0111.US

Ji-Yong PARK, *et al.*

Confirmation No.: 1632

Application No.: 10/779,781

Group Art Unit: 2815

Filed: February 18, 2004

Examiner: LANDAU, Matthew C.

For: FLAT PANEL DISPLAY DEVICE WITH POLYCRYSTALLINE SILICON THIN FILM TRANSISTOR

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. BOX 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Sir:

Applicants appreciate the indication in the Notice of Allowance mailed July 12, 2007 that claims 2-4, 7, 8, and 13 are allowed. While Applicants agree these claims are patentable over the cited references, Applicants do not agree that patentability resides in each feature exactly as expressed in the claims, nor that each feature is required for patentability of each claim.

Respectfully Submitted,

/hae-chan park/

Hae-Chan Park
Reg. No. 50,114

Date: October 12, 2007

H. C. Park & Associates, PLC
8500 Leesburg Pike
Suite 7500
Vienna, VA 22102
Tel: 703-288-5105
Fax: 703-288-5139
HCP/RTS/tmk